



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Marco A. Zuniga et al. Art Unit : 2838
Serial No. : 09/475,713 Examiner : Shawn Riley
Filed : December 30, 1999
Title : SWITCHING REGULATOR WITH MULTIPLE POWER TRANSISTOR
DRIVING VOLTAGES

Commissioner for Patents
Washington, D.C. 20231

RESPONSE TO RESTRICTION REQUIREMENT

In response to the Restriction Requirement mailed July 27, 2000, please amend the application as follows:

In the claims:

Please add the following new claims:

- SUP 27
B47
a/
19. (New) The voltage regulator of claim 1, wherein the first transistor includes a source, a drain and a gate, and the first transistor has a channel length between the source and the drain which is less than a channel length required for reliable behavior under steady state saturation conditions.
20. (New) The voltage regulator of claim 1, wherein the second transistor includes a source, a drain and a gate, and the second transistor has a channel length between the source and the drain which is less than a channel length required for reliable behavior under steady state saturation conditions.

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

August 8, 2000
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Signature

Matilde C. Sanchez
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